

**Amendment to the Specification**

In the specification, please amend Paragraph [023] as follows:

– [023] In general, the invention provides methods and circuits for decoupling the stability characteristics of loops within a circuit. Referring now primarily to Figure 3, an overview of the methods and circuits of the invention is shown by this example of a schematic diagram of a circuit 30 wherein the stability characteristics of an LDO loop 10 and current limit loop 12 have been decoupled. Transistor M4 is shown coupled between the opamp 20 and buffer 18. The gate is connected to the output of the error amplifier 20, the drain [, and] is connected to ground, and the source is connected to node N5. At node N5, a current source is coupled to a power supply and the compensation capacitor Cx of the comparator 16.--